

Notice of References Cited	Application/Control No. 09/977,994		Applicant(s)/Patent Under Reexamination NAGATA ET AL.	
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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-5,481,484	01-1996	Ogawa et al.	703/14
*	C	US-6,144,217	11-2000	Iwata et al.	326/27
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
*	U	Nagata et al., "Measurements and Analyses of Substrate Noise Waveform in Mixed Signal IC Environment". Proceedings of the IEEE 1999 Custom Integrated Circuits Conference. May 1999.			
*	V	Mitra et al., "Substrate-Aware Mixed-Signal Macrocell Placement in WRIGHT". IEEE Journal of Solid-State Circuits. Vol. 30. No. 3, March 1995.			
*	W	Su et al., "Experimental Results and Modeling Techniques for Substrate Noise in Mixed-Signal Integrated Circuits", IEEE Journal of Solid-State Circuits, Vol. 28, No. 4, April 1993.			
*	X	Mitra et al., "A Methodology for Rapid Estimation of Substrate-Coupled Switching Noise", IEEE 1995 Custom Integrated Circuits Conference.			

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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*	U	Charbon et al., "Modeling Digital Substrate Noise Injection in Mixed-Signal IC's", IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 18, No. 3, March 1999.
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